

FIG. 1

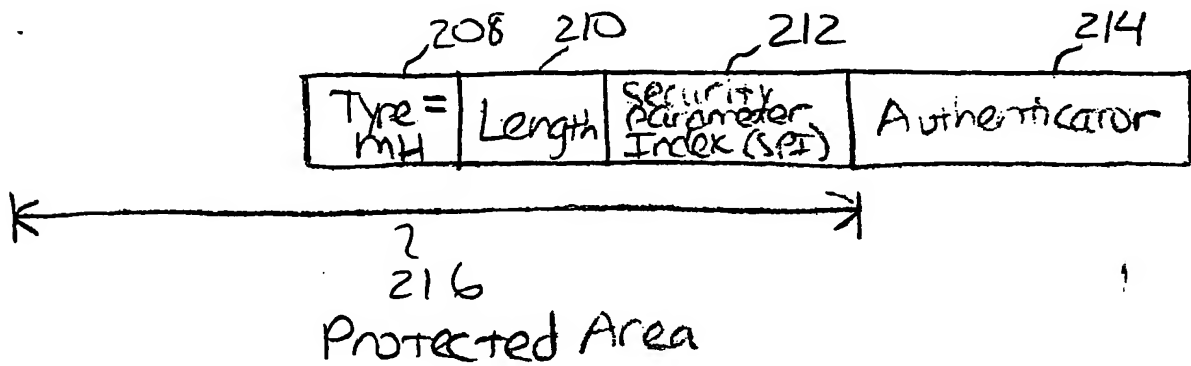
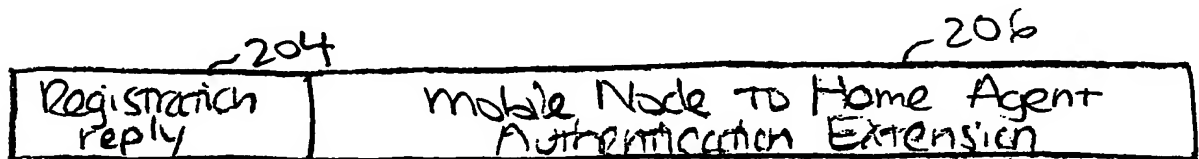
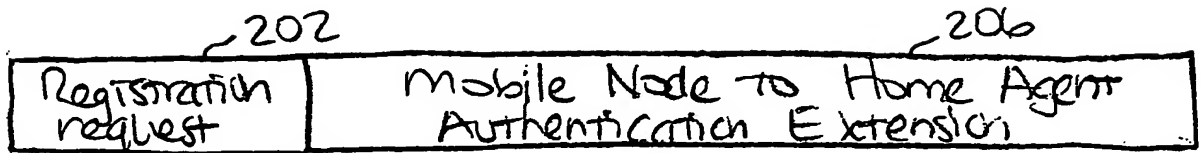


FIG. 2

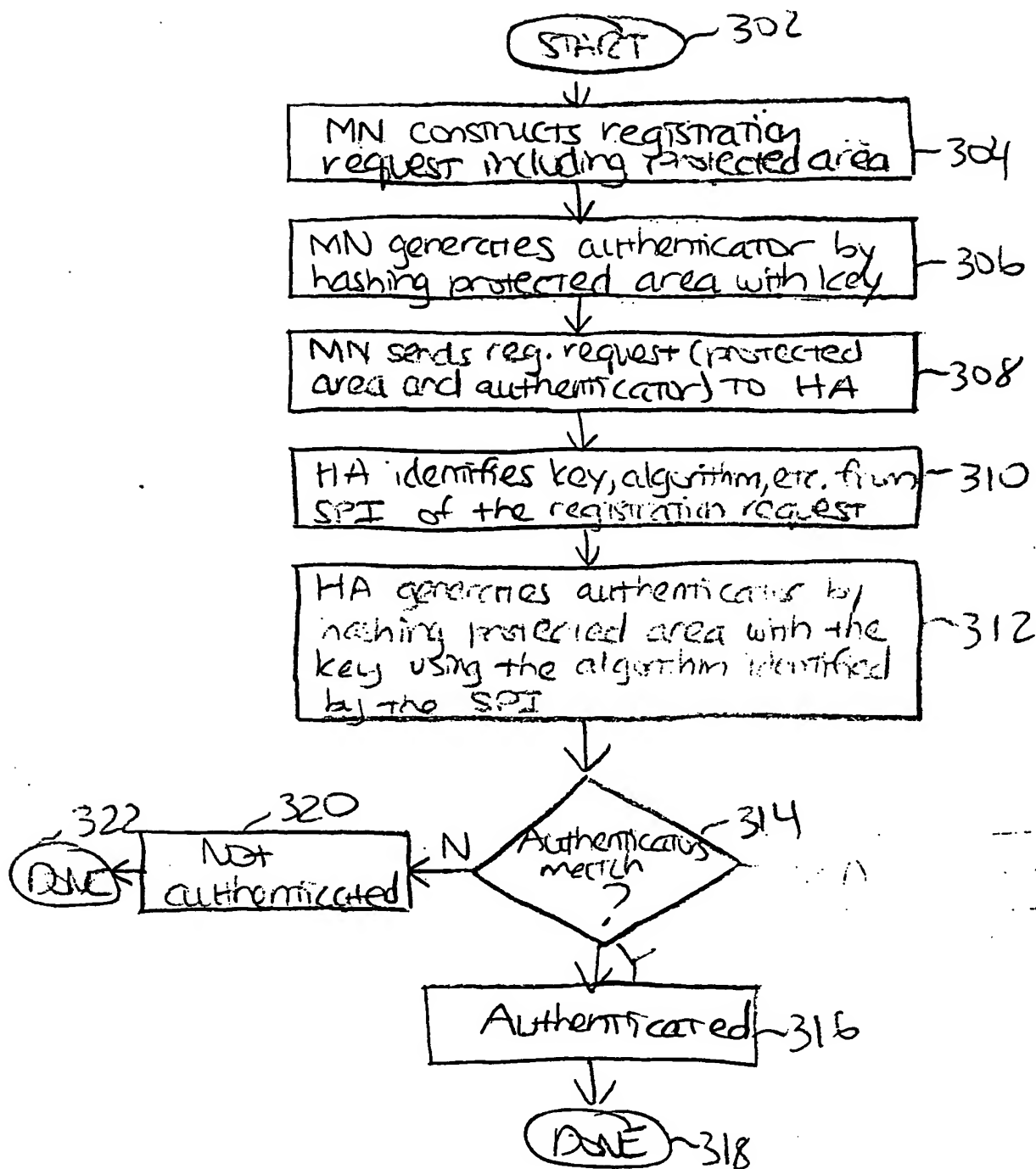


FIG. 3

404

402

406	408	410	412	414	416
MIN	SPI	Key	algorithm	mode	replay timer
IPaddr1	SPI1	key1	e.g., RSA	e.g., prefix-suffix	timestamp1
IPaddr2	SPI2	key2	e.g., RSA	e.g., prefix-suffix	timestamp2
IPaddr3	SPI3	key3	e.g., RSA	e.g., prefix-suffix	timestamp3
:	:	:	:	:	:
:	:	:	:	:	:
:	:	:	:	:	:
IPaddrN	SPI N	keyN	-	-	timestampN

FIG. 4

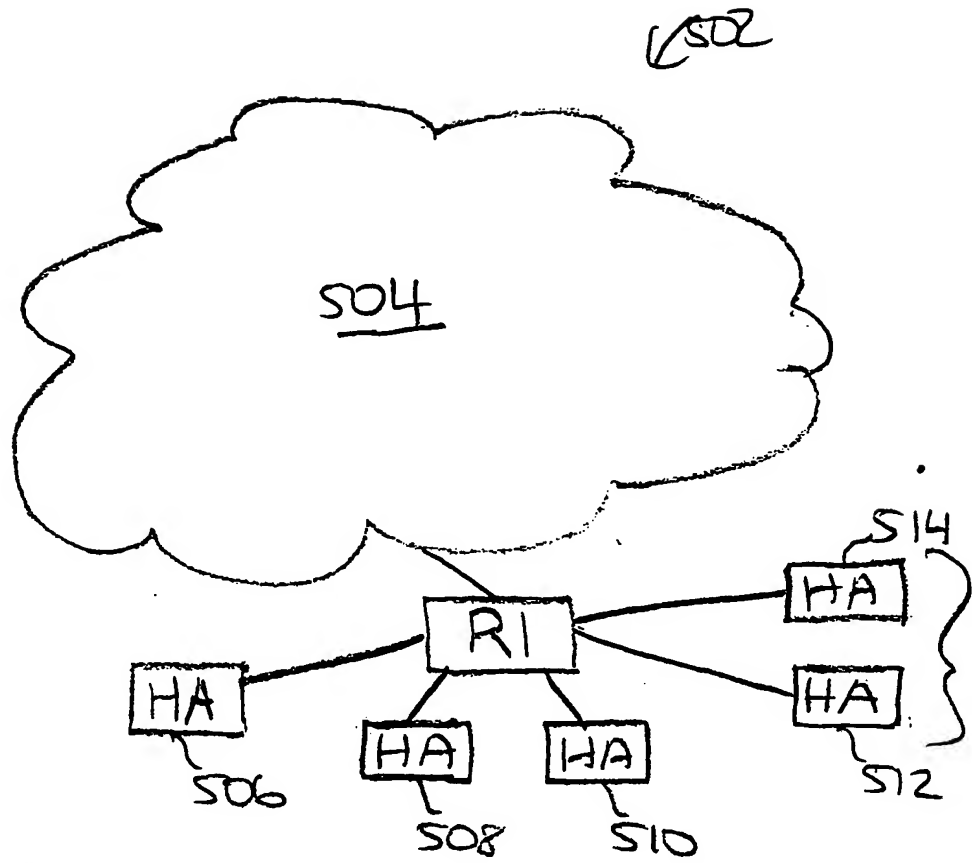


FIG. 5

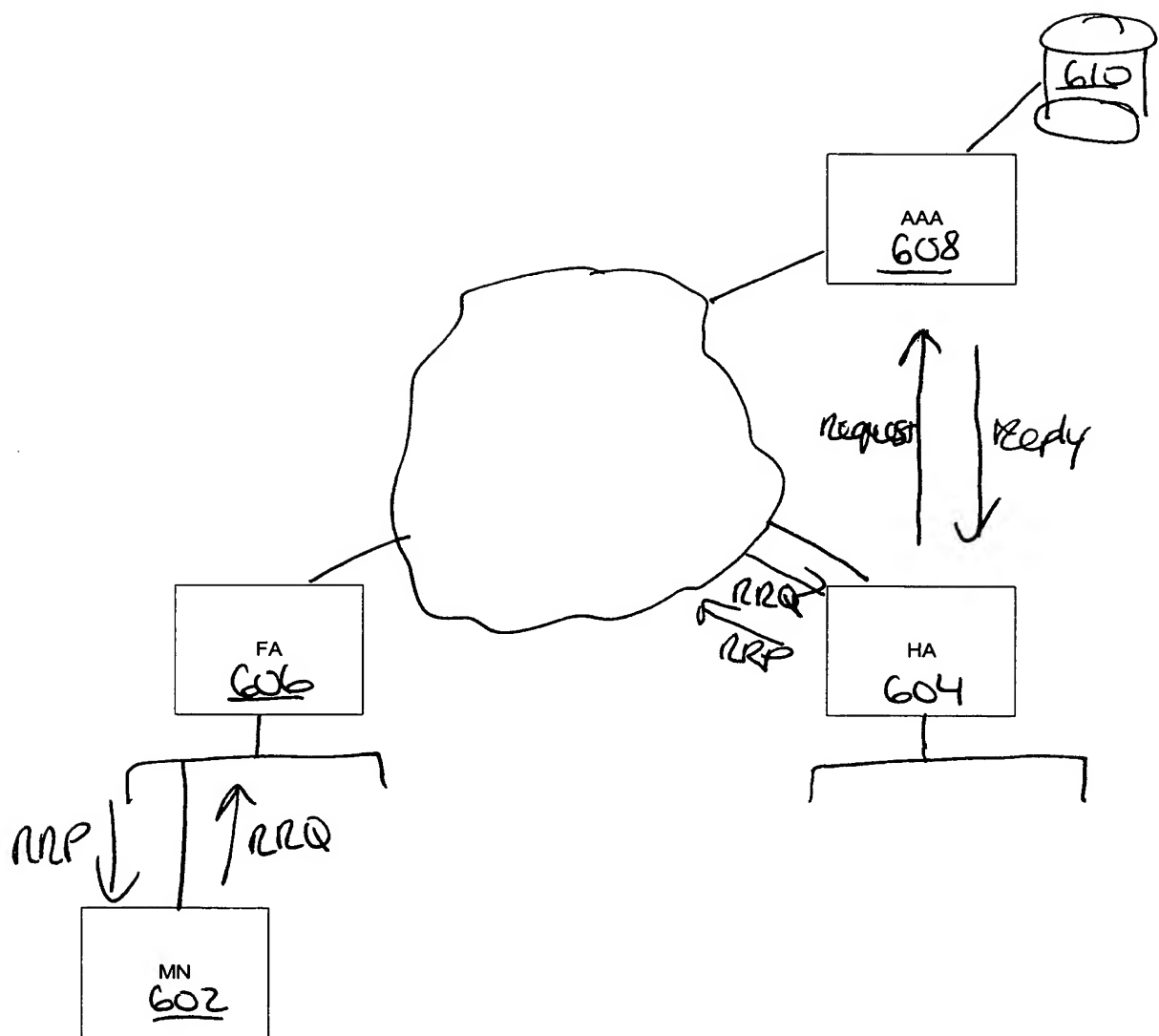


FIG. 6

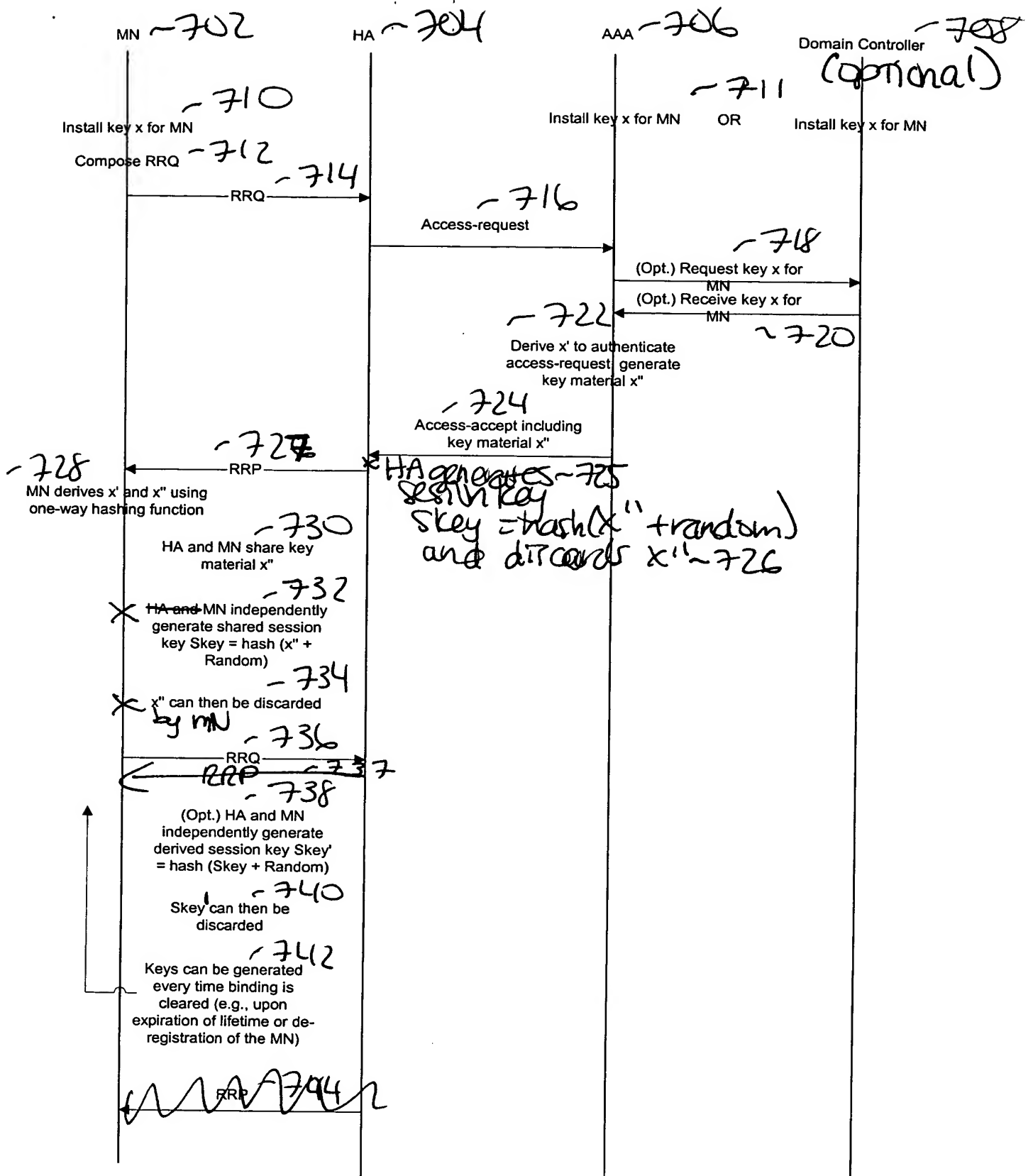
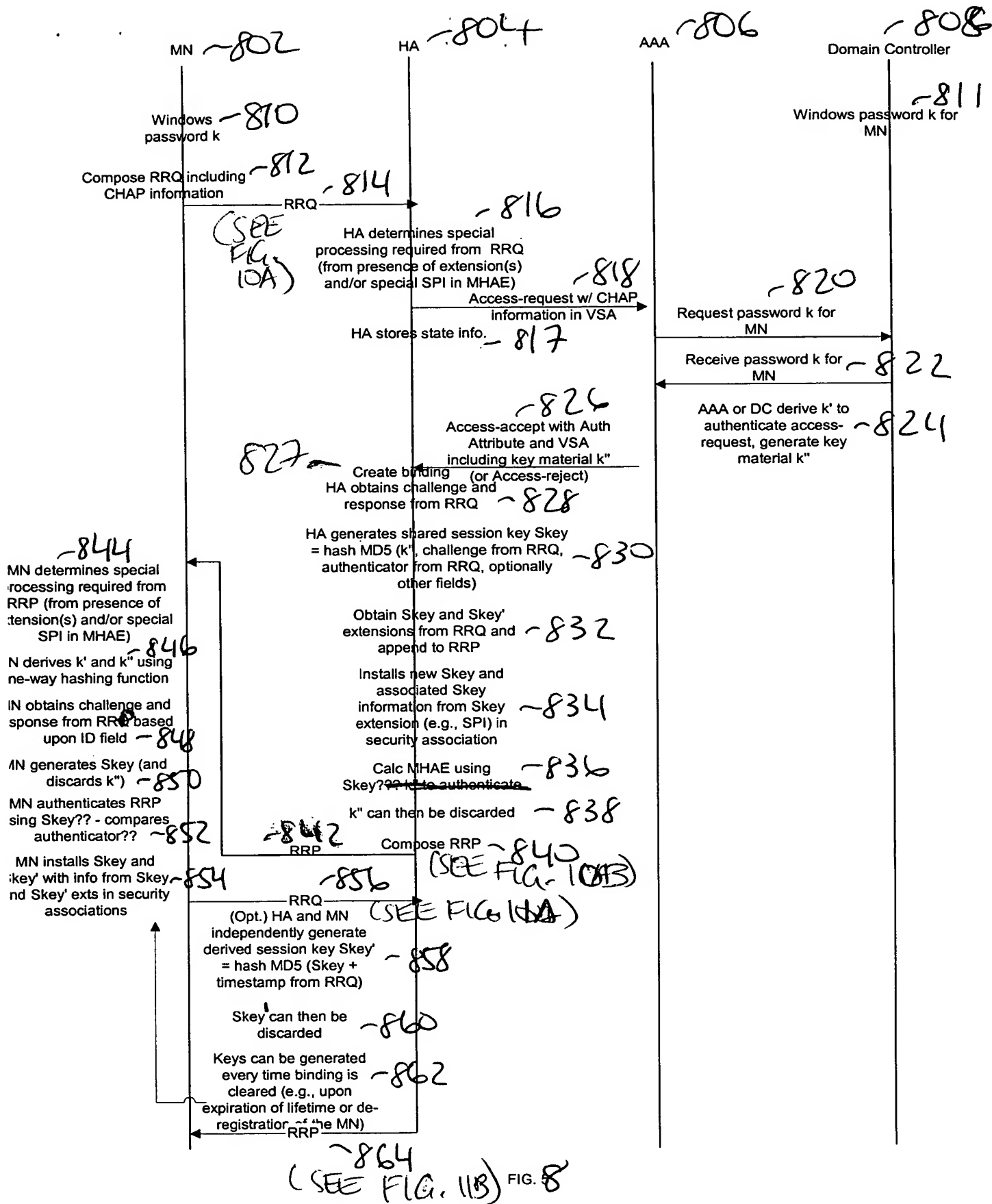


FIG. 4



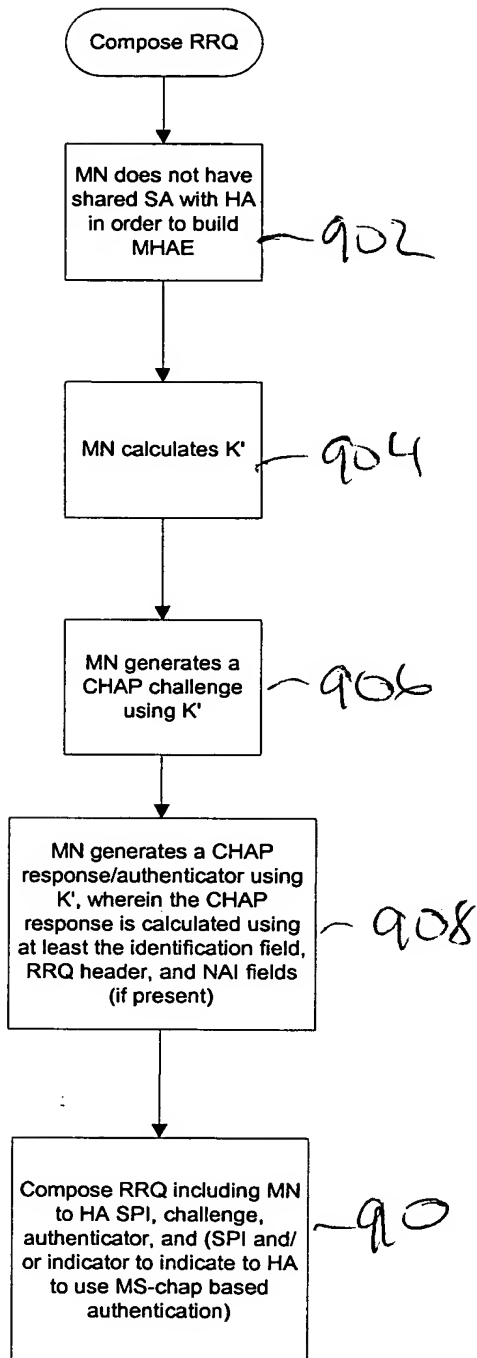


FIG. 8

1500

1008 1010 1012 1014 1016

1002 RRQ header	1004 NAI	1006 S-Key extension including SPI1, replay protection, and algorithm	S-Key extension including SPI2, replay protection, and algorithm - to THIS SENT IN FIRST MESSAGE	Challenge	Auth protocol	Authenticator/ challenge response	MHAE with sp cific SPI
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FIG. 10A

1100

1102 1104 1106 1108 1110

RRQ header	NAI	S-Key extension including SPI2, replay protection, and algorithm	MHAE using Skey	Timestamp
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FIG. 10B 11A

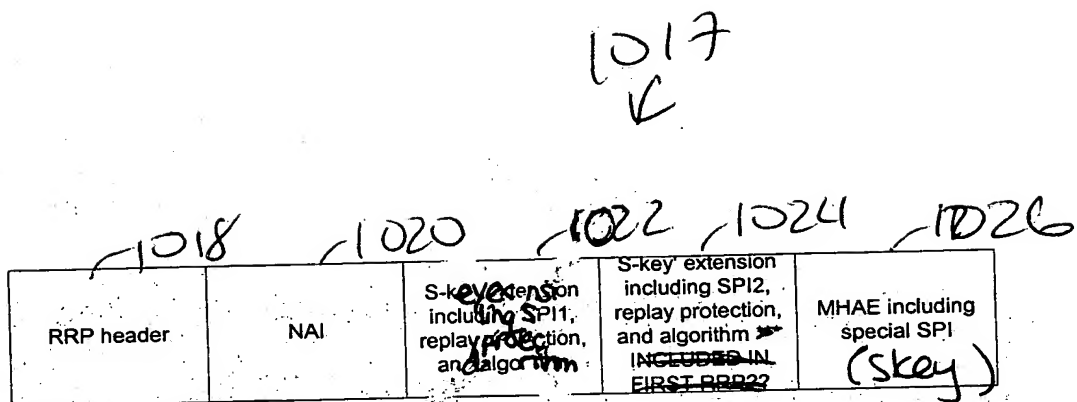


FIG. 10B

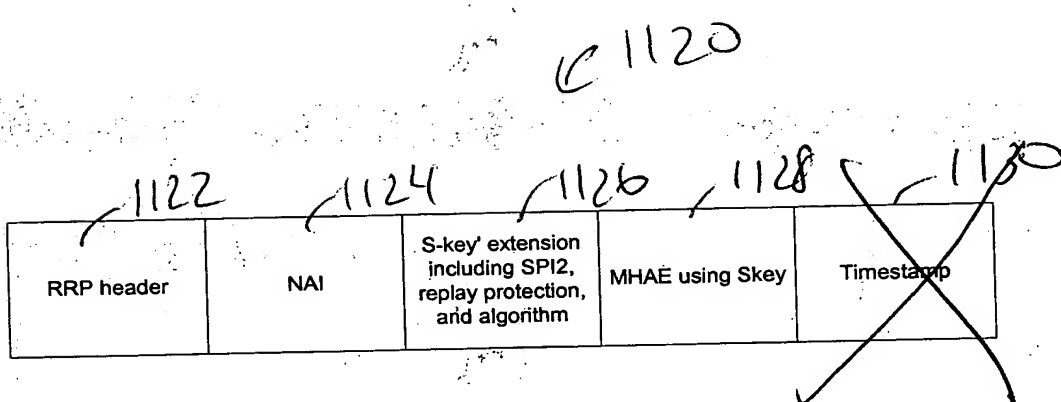


FIG. 11B

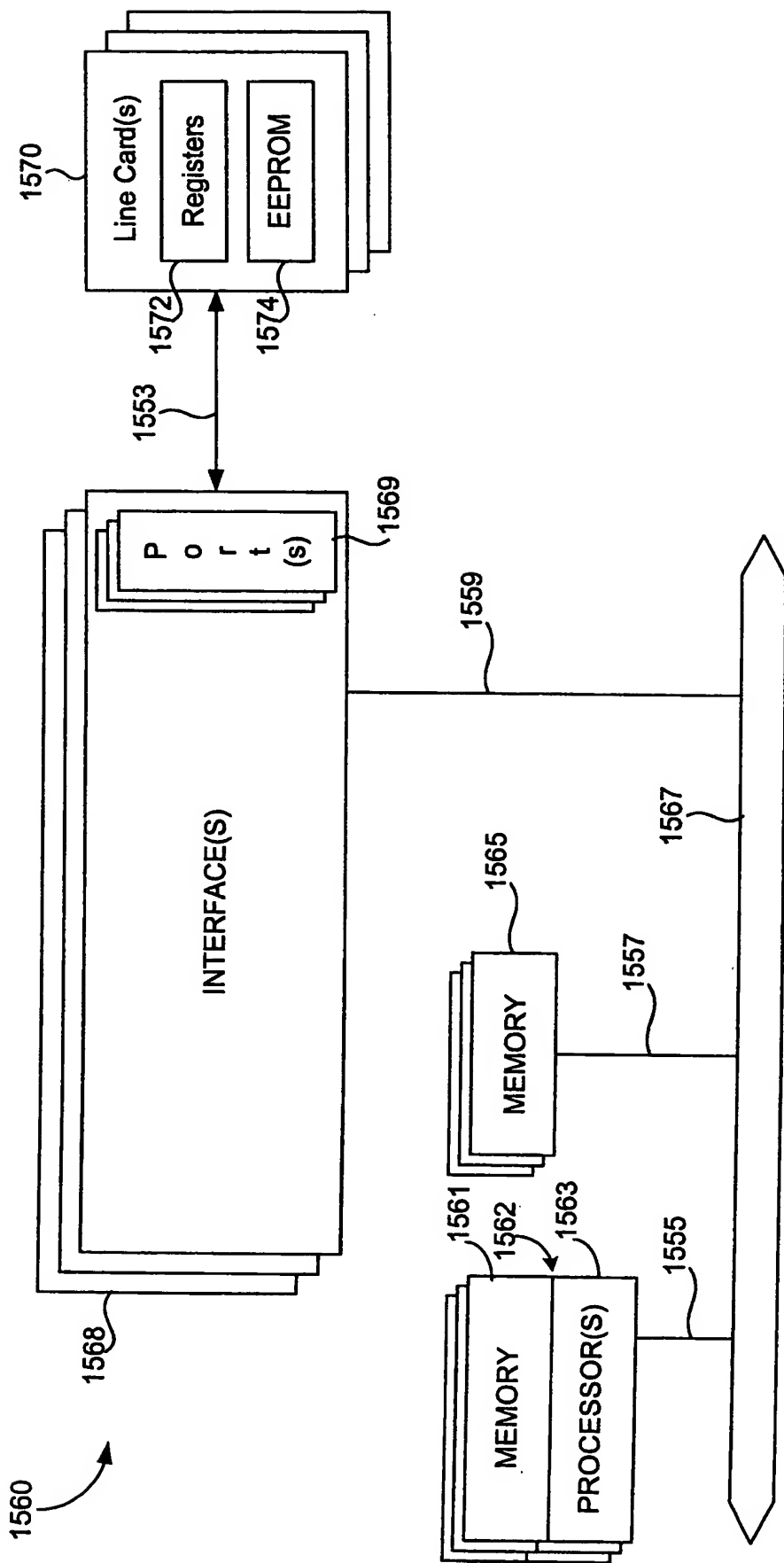


FIG. 12